

## Nexus JobManager

	•	s c									1	A large amount of painted objects may affect processing performance 'Lay4-1'
											4	A large amount of painted objects may affect processing performance "Lay4-3"
<b>8</b> 8 0	•	11 Id	Original data	Location		Progress		Priority	Submit time	Start tim		A large amount of painted objects may affect processing performance 'Lay4-4'
	aine -	5671	44245AAM-GBR.zip		heck Todo's	Review			2024-11-19 19:45:49.0	2024		
			odb0917120.tgz	😑 Edit in		Unexpect	edFailure		2024-11-19 16:08:18.0			A large amount of painted objects may affect processing performance 'Top_Silk'
	1887	5666	XS_V02_Z zip	🖸 QED W	/ith Image Data	Finished			2024-11-19 09:34:15.0	2024		A large amount of painted objects may affect processing performance 'Bot_Silk'
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				CHEST_TRI 😑 QED C		Review						
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-	1212	5660	XS_V02_Z.zip		/ith Image Data	Finished			2024-11-18 18:04:46.0	2024		
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Nexus JobManager is a client application designed for online, browser-based viewing of all job data generated during an Integr8tor Nexus run. This extensive information encompasses everything from the contents of the incoming archive to stackup and individual layer details, as well as fully detailed analysis results, including their locations on the board and automatically generated assembly or fabrication panel suggestions. Nexus JobManager presents this wealth of information through user-friendly and highly intuitive review tools.

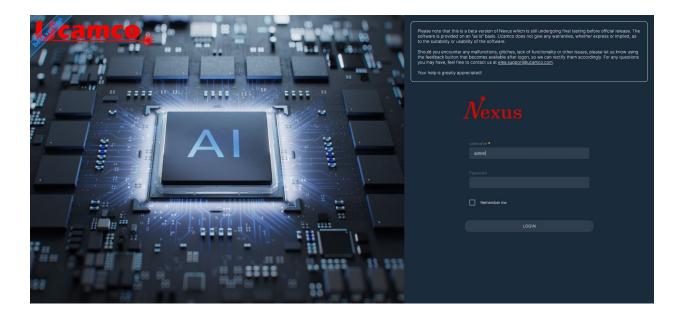
Standard functionalities include a range of job queue actions, such as submitting a job to the server, modifying or restarting a job, relocating a job, and aborting, unlocking, or removing a job from the queue when it is no longer relevant.

The reviewing capabilities of Nexus JobManager are not limited by client-oriented licenses; once a job has been successfully processed, the full job data can be accessed without restrictions. The software license configuration for the Integr8tor Nexus server determines the analysis results produced, and once generated, Nexus JobManager allows you to review this comprehensive data on its homepage or through dedicated Nexus JobManager viewers with just a click.

Nexus JobManager is a non-floatable license tied to a specific user, and system administrators control which user accounts have access to this app.

Nexus JobManager's full features and capabilities include:

- User authentication
  - Logging into the Nexus JobManager application and validating user credentials with the Integr8tor Nexus server



- Job Queue Viewing
  - Seeing the list of jobs currently available on the Integr8tor Nexus server, either in table or in tile view
  - Discovering which of these jobs are in progress, queueing up to be processed, require attention, have completed,...
  - Applying filters to reduce the full job queue to a subset of entries for easy retrieval of customer-specific archives, references, processing times, duplicate archives,...

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	5100. 1850	5419	admin	original_68-5570R87227.elea.zip	QED With Image Data	Finished	2024-10-07 17:59:48.0	2024-10-08 11:45:46.0	2024-10-08 11:59:49.0	14:03
	2200		admin	gerber-EPO241528_panel.zip	📮 QED With Image Data	Output Generated	2024-10-04 12:38:01.0	2024-10-07 14:47:16.0	2024-10-07 14:47:31.0	
<u>n</u>		5417	dirk	gerber-EPO241528_single.zip	QED With Image Data	Synced	2024-10-04 12:38:00.0	2024-10-10 17:39:47.0	2024-10-10 17:40:02.0	
			admin	089744.zip	QED With Image Data	Finished	2024-10-04 09:06:25.0	2024-10-04 09:06:30.0	2024-10-04 09:07:30.0	
	- <b>A</b>	5414	admin	089744.zip	QED With Image Data	Output Generated	2024-10-02 14:46:31.0	2024-10-04 09:01:35.0	2024-10-04 09:01:36.0	00:01
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		5412	admin	gerber-EPO248455.zip	QED With Image Data	Finished	2024-10-01 17:35:18.0	2024-10-03 12:10:06.0	2024-10-03 12:11:20.0	
			admin	gerber-EPO247770.zip	📮 QED With Image Data	Finished	2024-10-01 17:35:18.0	2024-10-03 15:31:43.0	2024-10-03 15:33:56.0	
		5410	admin	gerber-EPO245674.zip	QED With Image Data	Finished	2024-10-01 17:35:18.0	2024-10-07 14:03:34.0	2024-10-07 14:11:38.0	08:04
				gerber-EPO248962.zip	QED Check Todo's	Review	2024-10-01 17:35:18.0	2024-10-07 16:45:01.0	2024-10-07 16:47:58.0	
		5408	admin	gerber-EPO249450.zip	QED Check Todo's	Review	2024-10-01 17:35:18.0	2024-10-04 11:52:07.0	2024-10-04 11:54:26.0	02:19
			admin	gerber-EPO248052.zip	QED With Image Data	Finished	2024-10-01 17:35:18.0	2024-10-01 17:57:03.0	2024-10-01 18:03:15.0	
		5406	admin	gerber-EPO248312.zip	QED Check Todo's	Review	2024-10-01 17:35:18.0	2024-10-01 17:35:27.0	2024-10-01 17:39:34.0	04:07
		5405	admin	gerber-EPO247919.zip	QED With Image Data	Finished	2024-10-01 17:35:18.0	2024-10-01 18:16:22.0	2024-10-01 18:18:31.0	
		<sup>5</sup> 5404	admin	gerber-EPO244517.zip	QED With Image Data	Finished	2024-10-01 17:35:18.0	2024-10-04 11:32:04.0	2024-10-04 11:36:15.0	04:11
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	8	5402	admin	gerber-EPO244530.zip	QED With Image Data	Finished	2024-10-01 17:35:18.0	2024-10-04 11:51:27.0	2024-10-04 11:54:20.0	02:53
		5401	admin	gerber-EPO246659.zip	QED With Image Data	Finished	2024-10-01 17:35:18.0	2024-10-04 12:17:05.0	2024-10-04 12:19:48.0	
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## Table view

## Tile view



- Job Querying
  - Clicking a single queue entry to gain access to the product's top-level details:
    - Full contents of the incoming archive
    - Layer stackup image and functions, referenced to original and renamed PCB layer names
    - PCB top/bottom view, based on settings for solder mask, legend, and surface finish
    - Per-layer view of all PCB layers
    - Contents viewing of any text/PDF/Word/Excel/... document in the incoming archive
    - o Info/warning/error messages encountered while processing
    - Access to the Quotation and Engineering Data (QED) PDF report

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	5397	admin		O242654.zip		ED With Image Da		lished		2024-10-01 17:35:17.0	2024-10-04						
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¢ EN	50 N	E 845												interritive - v	02410-241007 Next	us v2-4/10/2024-16:22 Changelog 4	2004

- Add Job
  - Creating a new entry in the job queue by submitting an archive to the Integr8tor Nexus server, automatically triggering a data input and analysis
  - Entering any of the product's technical specifications already known at the time of submit
- Modify Job
  - Completing or changing the product's technical specifications from the Modify Job dialogue
  - Automated partial job reprocessing to bring images and QED PDF in line with updated info
- Restart Job
  - Restarting data input and analysis of a previously processed job, while keeping or changing technical specifications as needed
- Move Job
  - Making a job reprocess only a selected part of the entire data input and analysis process
- Continue Job
  - > Allowing a job that was stopped along the way to continue its route

- Abort Job
  - Interrupting a running job
- Remove Job
  - > Deleting an entry and its associated data from the job queue



- Layer Stackup Review
  - > Checking for full and correct layer stackup in a dedicated app
  - Extended layer attributes table with original layer names, renamed layer names, layer functions, layer positions, polarity, colors, base and finished copper thickness and etch compensation values
  - Fully dynamic, high resolution, multiple-layer PCB graphics with built-in zooming and measuring capabilities

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inner 3		.ay4-3				
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rout	C Renamed 	Inplated Function paste legend soldermaak outet inner linner	<ul> <li>top</li> <li>top</li> <li>top</li> <li>1</li> <li>2</li> <li>3</li> </ul>	positive     positive     positive     positive     positive     positive	0 0 0 0	
Fold	C Renumed Top.Paere Top.Sitk Lay4-1 Lay4-2 Lay4-3 Lay4-4 Lay4-4 Lay4-4	Punction Function paste logend soldermask outer inner inner outer	<ul> <li>top</li> <li>top</li> <li>top</li> <li>top</li> <li>2</li> <li>3</li> <li>4</li> </ul>	C positive positive positive positive positive positive positive		
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- Tec-spec Review
  - Dedicated application for checking correctness and completeness of technical specifications

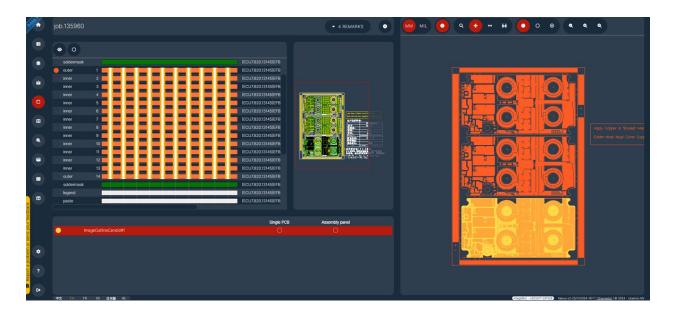
- Side-by-side display of tec-spec input form and archive documentation files contents
- Tracing AI-recognized technical specifications to their origin (under development)

							8)	Significant Characteristic	4	4
							r )	Process Characteristic	2	1
FabA032C279RevA_ContourNpth.ncd	FabA032C279RevA_Co					C	PTC )	Pass Through Characteristic	-	-
FabA032C279RevA_Npth.ncd	FabA032C279RevA_Npt.		non-plated					References		
FabA032C279RevA_Pth.ncd	FabA032C279RevA_Pth.					ZF Windet		Description		
FabA032C279RevA_IPC356.net	FabA032C279RevA_IPC	. lpc356	netref	bottom					250.02	
FabA032C279RevA_Drill.gbr	FabA032C279RevA_Drill.	ger274x	drillmap			DCF 17-02		Regulation of Special Characteristics (SC) Within th	e ZF Group	
FabA032C279RevA_Outline.gbr	FabA032C279RevA_Out.	. ger274x	cad_outline			ZFN 1127		Technical Specification for Printed Circuit Boards		
A0059W1022RevA.pdf			document			ZFN 9003		Control of Prohibited and Regulated Substances		
								Technical Requires	ments	
FabA032C279RevA_Known_Shorts_Opens.txt						Item	Applicability	Requirement		Special
matrix		matrix	document			No	Applicable	PCB application: Automotive Steering		Characteristic
						2	Applicable	General PCB requirements shall be according to	e ZFN 1127	
						3	Applicable	PCB shall be fabricated according to IPC 6012		
Technology						4	Applicable	Cleanliness shall be according to ZFN 1127 sec	tions 6.2.1, 6.2.2, 7.1, 7.2, 7.3	
						5	Applicable	Electrical testing shall be according to ZFN 112	27 section 6.2	
						6	Applicable	PCBs or panel shall be marked with a clearly v	isible electrical test pass mark	
	0				0	7	Applicable	All materials shall be UL qualified according to	s ZFN 1127 section 4.14	
						8	Applicable	Prohibited substances shall be controlled accord	ding to ZFN 9003	
		( ·				9	Applicable	Laminate material shall be according to IPC-41	01/99 Tg150 DegC MIN	
		FR4 TG				10	Applicable	Laminate material shall be Shengyi Autolad I		
	٥		135 Improved			11	Applicable	Surface finish shall be immersion Tin according		(81)
						12	Applicable	Solder mask shall be according to ZFN 1127 se Solder mask qualification and performance sha		
		FR4 TG				13	Applicable	ZFN 1127 section 6.11	a he according to	
Edge Plating		FR4 TG				14	Applicable	Solder musk material shall be Triyo PSR-4000	AM03TS	
			175 + Carbonfibre			15	Applicable	Solder mask thickness shall be according to ZF		
		Nelco 9	000 Series			16	Applicable	Allowable solder mask missegistration shall be section 6.9	according to ZFN 1127	
		Nelso 4	000-13 (teflon)			17	Not Applicable	Silk screen (legend ink) material shall be < Star	to Silk Screen Material >	
	0					18	Applicable	Allowable hole position variation shall be accord		( P2 )
			100-2HT (polyamide)			19	Applicable	Copper plating thickness of compliant pin hole the PCB drawing shall be according to ZFN 11.	s identified in the drill table of 27 section 6.13	82
		Rogers F	10-4003 (tefion)			20	Applicable	Minimum copper plating thickness in all mecha and buried plated holes shall be according to ZI	inically drilled plated through	
Impedance		Rogers	1002 (teflon)			21	Not Applicable	Minimum copper plating thickness in all mecha	anically drilled blind plated	
		Rogers 6	i000 (teflon)					holes shall be 20 µm Minimum copper plating thickness in all laser of	frilled elated holes shall be	
		Rogers	I-Flex 3000 (LPC)			22	Not Applicable	according to ZFN 1127 section 6.13		
		Taconic				23	Not Applicable Applicable	< State all applicable layers > Mind micro-vias Panel outline, circuit outline and internal shape		

- Drill Data Review
  - > Reviewing correct drill tool attribute assignments:
    - $\circ~$  End diameter and tool diameter
    - Plated/non-plated recognition
    - o Laser drilling recognition
    - Tool function (via, component,...)
    - Hole and slot counts



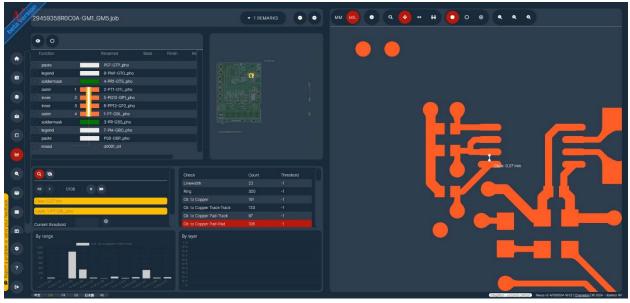
- Outline Review
  - Reviewing one or several potential outline candidates the system has discovered while searching for the best possible PCB or assembly panel outline



- Sizes Review
  - Reviewing an assembly panel layout, either manually defined or resulting from the assembly panel auto-recognition server module (licensed – under development)
  - Checking the correctness of the number of repeats in X and Y, the step/clearance values, and the size and location of the rail areas
  - Acknowledging the correct single image definition in case of incoming flat panel data

	F_UHF_2024103	1_1113gerber.job_84012539005	• 9 REMARKS	<u>₩</u> № <b>0 4 <del>6</del> • # <b>0</b> 0 <b>0 4 4 4</b></b>
Function       Reamed       See       Funch       Medianell         under       Tip:/Jaka       Tip:/Jaka       Tip:/Jaka         under       1       Lugle 1       Tip:/Jaka         under       1       Lugle 2       Tip:/Jaka         under       0       Lugle 2       Tip:/Jaka	• •			
soldsmusk     and     and <td< th=""><th>Function</th><th>Renamed Base Finish Material</th><th><b>这时,我出来了</b></th><th></th></td<>	Function	Renamed Base Finish Material	<b>这时,我出来了</b>	
axtr 1   axtr 1   axtr 0   axtr 0 <td></td> <td>Top_Paste</td> <td></td> <td></td>		Top_Paste		
www       2       1.496 2         www       3       1.496 4         user       5       1.496 4         user       5       1.496 4         user       5       1.496 4         user       6       1.496 4         user       1.496 4       1.496 4	soldermask	Top_Mask		
<pre>www b b by by b b by by b b by by b b by by</pre>		Lay6-1	0	
www       4       Lup6 4         www       5       Lup6 4         www       6       Lup6 4         wodormak       Bock,Mark         pate       Bock,Mark         meed       at         *       's meen	inner 2	Lay6-2		
www.s.s.l.uple.5   u.bel.4   u.bel.4 <td></td> <td>Lay6-3</td> <td></td> <td></td>		Lay6-3		
outer       0       1.96 6         sedemesk       Dic/Lask         Dick       Dic/Lask         Dick       Dick         Biscone       Image: Spaceng         Spaceng       0.0000         Spaceng <td>inner 4</td> <td>i 🔜 Lay6-4</td> <td></td> <td></td>	inner 4	i 🔜 Lay6-4		
sdorrusk bol Mak past Bol Mak di di d		i 🚃 🛄 Lay6-5		
sodemaak be, kaak pate b, kaak gate b, baak di berneme sodemaak be, baak di berneme sodemaak be, baak sodemaak berneme sodemaak be	outer 6	i 📕 Lay6-6		
mead       dt         • Spacing and step         • Spacing       0.0000         0 0000       1.5500         0 0000       1.5500         Step       9165       -238.2         Bodar       8.7760         9.9251       7.544				
x         x           Spacing and step         x           Spacing         00000           Space         15500           Space         9155           Brider definition         19251           Border         91700	) paste	Bot_Paste		
Respective         X         Y           Specing         00000         15600           Syp         9465         -2382           Border         80000         -2382           Border         81760         -2012           Border         94251         72544		df		
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x         y           Spacing         0.0000         1.6900           Ship         91.65         -238.2           Border definition				
x         y           Spacing         00000         15600           Step:         91.65         -238.2           Border definition         1000         1000           Border:         8.1760				
x         y           Spacing         00000         15600           Step:         91.65         -238.2           Border definition         1000         1000           Border:         8.1760	Searcing and stars			
Spacing:         0.0000         1.5600           Step:         91.65         -238.2           Border definition         8.1760         9.2531         7.844	O opacing and step			
Spacing:         0.0000         1.5600           Ship:         91.65         -238.2           Border definition		X		
Step:         9165         -2382           Border definition				
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Border: 8,2760 9,9251 7,2543	Step:			
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Border: 81700 9.9251 72143				
9.9251 76148 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Border definition			
9.9251 7.0148 • • • • • • • • • • • • • • • • • • •	Border:		81760	
> BOARD FLOW DIRECTION				
76/4				BOARD FLOW DIPECTION

- Hotspot Review (Integr8tor Nexus Hotspot server license required for generating analysis data)
  - Reviewing smallest values from the analysis results and locating them on the board
  - Supporting fully dynamic, high-resolution PCB graphics with on-screen feedback
  - Including feature measurements and query functionality to validate analysis results
  - > Filtering on hotspot type, PCB layer or value range



- Capabilities Review (Integr8tor Nexus DFM Classes server license required for generating analysis data)
  - > Consulting the results of a board's manufacturability analysis
  - Locating the features on the board responsible for pushing it into a higher manufacturability class
  - Supporting fully dynamic high-resolution PCB graphics with on-screen manufacturing class violation feedback
- > Including feature measurement and query capabilities

- Stackup library consultation (Integr8tor Nexus Stackup Template Library server license required)
  - Checking existing stackups in the library on the server to match customer requirements in terms of
    - Finished stackup thickness
    - Layer count
    - Base material requirements
    - PCB type
    - Copper weights
    - Tg value



Identifying "closest fit" if no 100% match turns out available

- Panel Setup and Layouts Review (Integr8tor Nexus Dynamic Panel Optimizer server license required for generating analysis data)
  - Reviewing different assembly and fabrication panel layout solutions, optionally with the corresponding material sheet cutting proposals
  - > Identifying the solutions that provide best yield or material utilization
  - Dimensioned drawings and real product PDF panel images with textual feedback

