

Nexus JobAdjuster

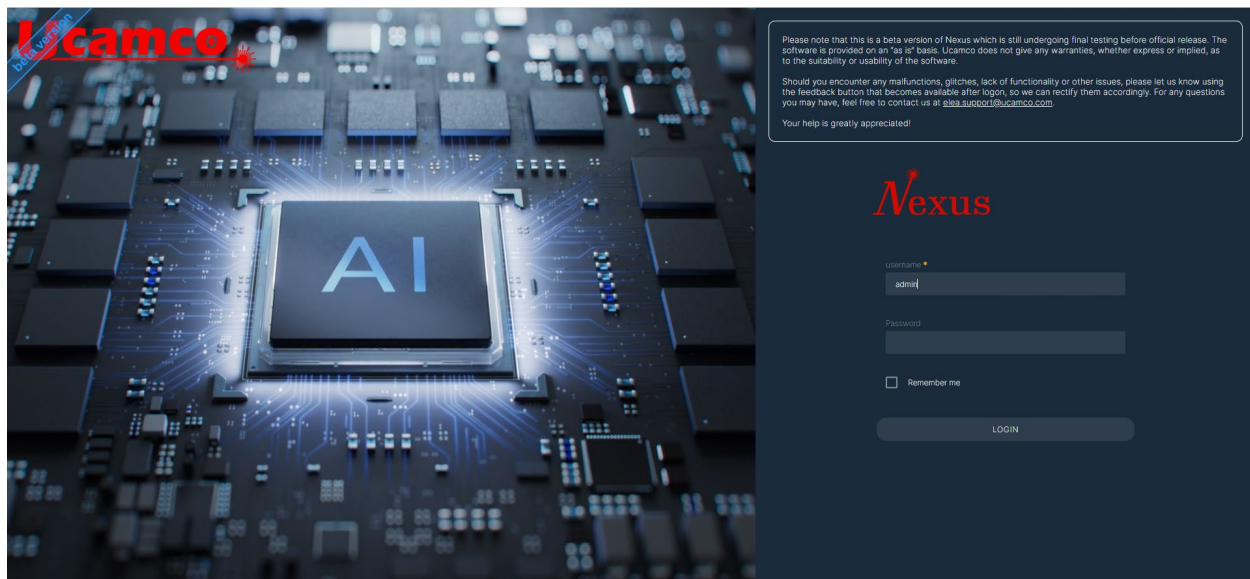
The screenshot displays the Nexus JobAdjuster software interface. At the top, it shows the user is logged in as 'Administrative User'. The main window is divided into several sections:

- Job List:** A table listing various jobs with columns for ID, Name, Location, Progress, Priority, Submit time, and Start time. The job '5597 TCU_R7_Z.zip' is highlighted in red.
- File List:** A table listing files with columns for Renamed, Function, Initial, Format, Position, Polarity, Size, and Full path. Files include 'Top_Silk', 'Top_Mask', 'Lay4-1', 'Lay4-2', 'Lay4-3', 'Lay4-4', 'Bot_Mask', 'Bot_Silk', 'Unplated', and 'dri'.
- Message Box:** A notification on the right side stating: 'Gerber import: Self-intersecting contours are detected, continuing with an interpretation of the contours. "CC-TCU-R7GBL" (at line 95029)'.
- PCB Layout View:** A 3D visualization of a green PCB layout with various components and traces.

Nexus JobAdjuster provides extensive, browser-based viewing of job data and analysis results, along with a comprehensive suite of data editing functions to address any issues that may hinder successful analysis. Similar to Nexus JobManager, the capabilities of Nexus JobAdjuster are not constrained by client-oriented licenses. Instead, the software configuration of the Integr8tor Nexus server determines the analyses performed. If any problems arise or results need fine-tuning, dedicated Nexus JobAdjuster apps are available to handle these tasks simply and efficiently. Moreover, Nexus JobAdjuster offers exceptional versatility and integration, connecting seamlessly to its UcamX Workflow Edition and SpeedStack client applications through a new data transfer interface that eliminates the need for Windows shares.

Nexus JobAdjuster's full features and capabilities include:

- User authentication
 - Logging into the Nexus JobAdjuster application and validating user credentials with the Integr8tor Nexus server



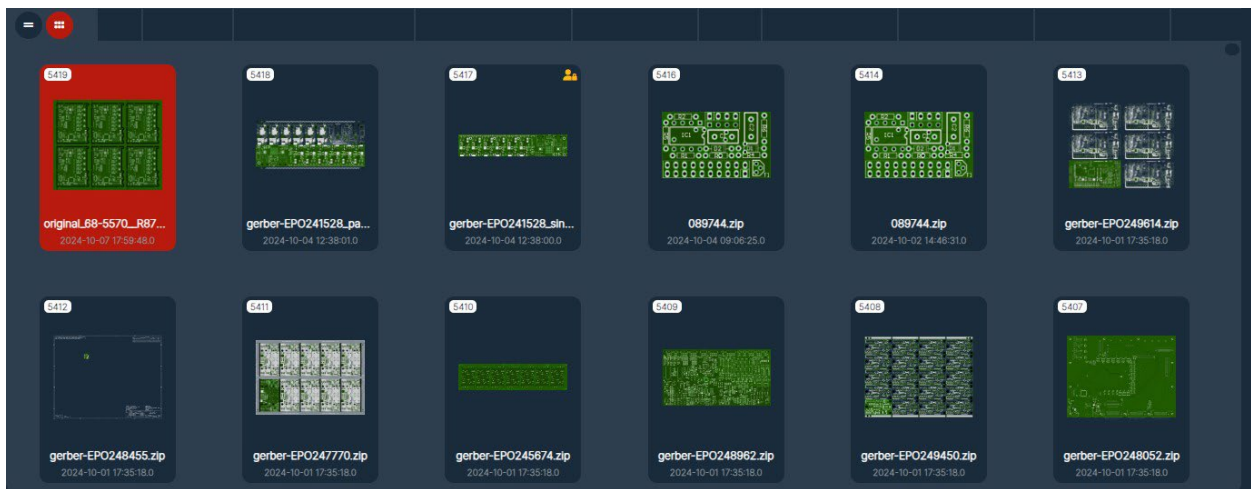
- Job Queue Viewing
 - Seeing the list of jobs currently available on the Integr8tor Nexus server, either in table or in tile view
 - Discovering which of these jobs are in progress, queueing up to be processed, require attention, have completed,...

- Applying filters to reduce the full job queue to a subset of entries for easy retrieval of customer-specific archives, references, processing times, duplicate archives, ...

Table view

id	Owner	Original data	Location	Progress	Priority	Submit time	Start time	Finish time	Last duration
5419	admin	original_68-5570_R87227.eles.zip	QED With Image Data	Finished	3	2024-10-07 17:59:48.0	2024-10-08 11:45:46.0	2024-10-08 11:59:49.0	14:03
5418	admin	gerber-EPO241528_panel.zip	QED With Image Data	Output Generated	3	2024-10-04 12:38:01.0	2024-10-07 14:47:16.0	2024-10-07 14:47:31.0	00:15
5417	dirk	gerber-EPO241528_single.zip	QED With Image Data	Synced	3	2024-10-04 12:38:00.0	2024-10-10 17:39:47.0	2024-10-10 17:40:02.0	00:15
5416	admin	089744.zip	QED With Image Data	Finished	3	2024-10-04 09:06:25.0	2024-10-04 09:06:30.0	2024-10-04 09:07:30.0	01:00
5414	admin	089744.zip	QED With Image Data	Output Generated	3	2024-10-02 14:46:31.0	2024-10-04 09:01:35.0	2024-10-04 09:01:36.0	00:01
5413	admin	gerber-EPO249614.zip	QED With Image Data	Output Generated	3	2024-10-01 17:35:18.0	2024-10-04 08:58:23.0	2024-10-04 08:58:28.0	00:05
5412	admin	gerber-EPO248455.zip	QED With Image Data	Finished	3	2024-10-01 17:35:18.0	2024-10-03 12:10:06.0	2024-10-03 12:11:20.0	01:14
5411	admin	gerber-EPO247770.zip	QED With Image Data	Finished	3	2024-10-01 17:35:18.0	2024-10-03 15:31:43.0	2024-10-03 15:33:56.0	02:13
5410	admin	gerber-EPO245674.zip	QED With Image Data	Finished	3	2024-10-01 17:35:18.0	2024-10-07 14:03:34.0	2024-10-07 14:11:38.0	08:04
5409	dirk	gerber-EPO248962.zip	QED Check Todo's	Review	3	2024-10-01 17:35:18.0	2024-10-07 16:45:01.0	2024-10-07 16:47:58.0	02:57
5408	admin	gerber-EPO249450.zip	QED Check Todo's	Review	3	2024-10-01 17:35:18.0	2024-10-04 11:52:07.0	2024-10-04 11:54:28.0	02:19
5407	admin	gerber-EPO248052.zip	QED With Image Data	Finished	3	2024-10-01 17:35:18.0	2024-10-01 17:57:03.0	2024-10-01 18:03:15.0	06:12
5406	admin	gerber-EPO248312.zip	QED Check Todo's	Review	3	2024-10-01 17:35:18.0	2024-10-01 17:35:27.0	2024-10-01 17:39:34.0	04:07
5405	admin	gerber-EPO247919.zip	QED With Image Data	Finished	3	2024-10-01 17:35:18.0	2024-10-01 18:16:22.0	2024-10-01 18:18:31.0	02:09
5404	admin	gerber-EPO244517.zip	QED With Image Data	Finished	3	2024-10-01 17:35:18.0	2024-10-04 11:32:04.0	2024-10-04 11:36:15.0	04:11
5403	admin	gerber-EPO243845.zip	QED With Image Data	Finished	3	2024-10-01 17:35:18.0	2024-10-04 11:50:16.0	2024-10-04 11:53:27.0	03:11
5402	admin	gerber-EPO244530.zip	QED With Image Data	Finished	3	2024-10-01 17:35:18.0	2024-10-04 11:51:27.0	2024-10-04 11:54:20.0	02:53
5401	admin	gerber-EPO246659.zip	QED With Image Data	Finished	3	2024-10-01 17:35:18.0	2024-10-04 12:17:05.0	2024-10-04 12:19:48.0	02:43
5400	admin	gerber-EPO240766.zip	QED With Image Data	Finished	3	2024-10-01 17:35:18.0	2024-10-04 12:13:53.0	2024-10-04 12:17:13.0	03:20

Tile view



- Job Querying

- Clicking a single job queue entry to access its top-level details:
 - Full contents of the incoming archive
 - Layer stackup image and functions, referenced to original and renamed PCB layer names
 - PCB top/bottom view, based on settings for solder mask, legend, and surface finish
 - Per-layer view of all PCB layers
 - Contents viewing of any text/PDF/Word/Excel/... document in the incoming archive

- Info/warning/error messages encountered while processing
- Access to the Quotation and Engineering Data (QED) PDF report

The screenshot displays the Integr8tor software interface. On the left, a job queue table lists various processing tasks. The selected job, 'gerber-EPO242654.zip', is highlighted in red. The table columns include Renamed, Function, Initial, Format, Position, Polarity, Size, and Full path. On the right, a 3D model of a PCB is shown, featuring a grid of components and a central chip. The interface includes navigation icons and a QED button.

Renamed	Function	Initial	Format	Position	Polarity	Size	Full path	
gtp_gbr	paste	gtp_gbr	ger274x	top		15270	C:\Users\cam\data\8_output\CAPE\5397\unpacked\1	
gto_gbr	legend	gto_gbr	ger274x	top		56248	C:\Users\cam\data\8_output\CAPE\5397\unpacked\1	
gts_gbr	soldermask	gts_gbr	ger274x	top	positive	117443	C:\Users\cam\data\8_output\CAPE\5397\unpacked\1	
gtL_gbr	1	outer	gtp_gbr	ger274x	1	positive	2939394	C:\Users\cam\data\8_output\CAPE\5397\unpacked\1
gtZ_gbr	2	inner	gtz_gbr	ger274x	2	positive	1403649	C:\Users\cam\data\8_output\CAPE\5397\unpacked\1
gtB_gbr	3	inner	gtb_gbr	ger274x	3	positive	1592881	C:\Users\cam\data\8_output\CAPE\5397\unpacked\1
gtO_gbr	4	outer	gto_gbr	ger274x	4	positive	3404018	C:\Users\cam\data\8_output\CAPE\5397\unpacked\1
gbo_gbr	soldermask	gbo_gbr	ger274x	bottom	positive	216727	C:\Users\cam\data\8_output\CAPE\5397\unpacked\1	
gbo_gbr	legend	gbo_gbr	ger274x	bottom		457850	C:\Users\cam\data\8_output\CAPE\5397\unpacked\1	
gbo_gbr	paste	gtp_gbr	ger274x	bottom		22885	C:\Users\cam\data\8_output\CAPE\5397\unpacked\1	
dit_gbr	mixed	dit_gbr	ger274x	1-4		15439	C:\Users\cam\data\8_output\CAPE\5397\unpacked\1	
gto1_gbr	non-plated	gto1_gbr	ger274x	1-4		3638	C:\Users\cam\data\8_output\CAPE\5397\unpacked\1	

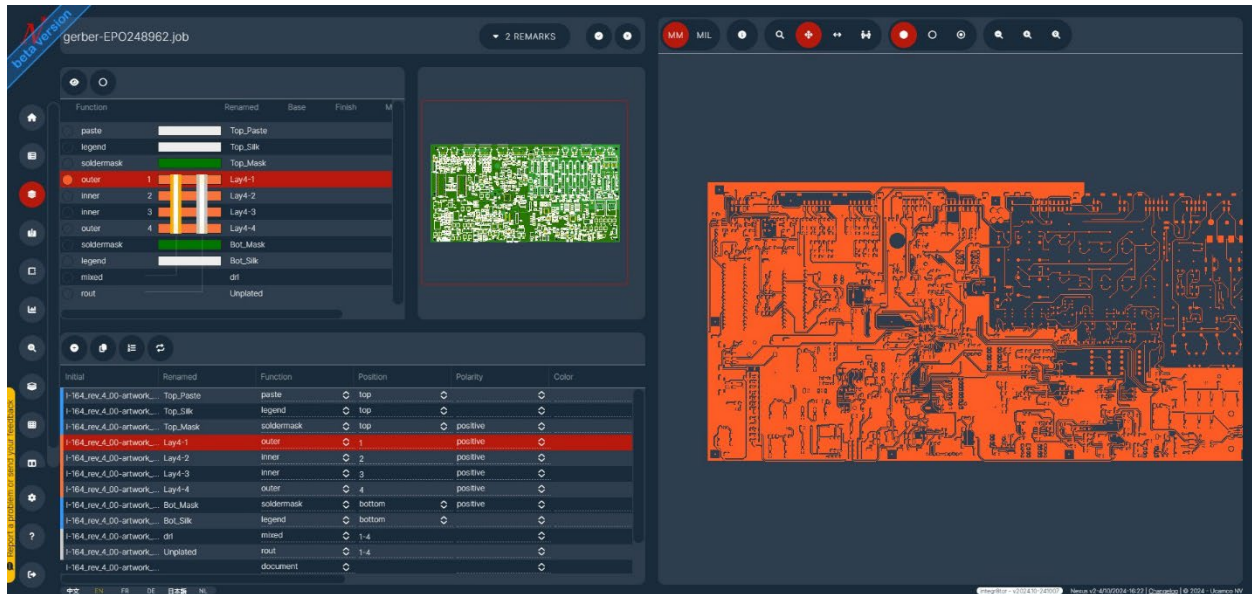
- Add Job
 - Creating a new entry in the job queue by submitting an archive to the Integr8tor Nexus server, automatically triggering a data input and analysis
 - Entering any of the product's technical specifications already known at the time of submit
- Modify Job
 - Completing or changing the product's technical specifications from the Modify Job dialogue
 - Automated partial job reprocessing to bring images and QED PDF in line with updated info
- Restart Job
 - Restarting data input and analysis of a previously processed job, while keeping or changing technical specifications as needed
- Move Job
 - Making a job reprocess only a selected part of the entire data input and analysis process

- Accept Remarks
 - Acknowledging Job Remarks issued by the server during processing, tidying-up all Job Remark references in the QED report
- Continue Job
 - Allowing a job that was stopped along the way to continue its route
- Abort Job
 - Interrupting a running job
- Remove Job
 - Deleting an entry and its associated data from the job queue
- Unlock Job
 - Removing an unintended job lock to make the job available for further use
- **Links to external applications**
 - **Bi-directional hotlink to *UcamX Workflow Edition (WE)* for full-scale DPF layout editing capabilities. Note that this functionality requires a *UcamX_WE* version v2024.11-2412XX or higher**
 - **Bi-directional hotlink to Polar Instruments' *SpeedStack Stack up Builder* tool for advanced base material handling, stackup DRC's and before-and-after pressing material thickness calculations (Polar license required)**



- Layer Stackup Review and Edit
 - Layer stackup reviewing and editing in a dedicated application
 - Extended layer attributes table with original layer names, renamed layer names, layer functions, layer positions, polarity, colors, base and finished copper thickness and etch compensation values
 - **Editing of any of these layer attributes**
 - **Drag and drop functionality for easy corrections in case of incorrect stackup**

- **Consolidating all modifications in the job on the Integr8tor Nexus server with a (partial) rerun**
- Fully dynamic, high resolution, multiple-layer PCB graphics with built-in zooming and measuring capabilities



- **Tec-spec Review and Edit**
 - Dedicated application for entering or reviewing a product's technical specifications
 - **Full functionality and information storage available even when a job is still running to speed up the entire process**
 - **Manually drag and drop values from documentation files to the Tec-spec input form**
 - Side-by-side display of Tec-spec input from and archive documentation files contents
 - **Optionally extendable with AI-powered PDF Xtract plug-in for automatic extraction of technical specifications**
 - Tracing AI-recognized technical specifications to their origin (under development)
 - **Storing Tec-spec results with the job on the Integr8tor Nexus server**

Re_Volvo_SPA3.3_C1_PFS_PCB.job

Initial	Renamed	Format	Function	Position	Pr
FabA032C279Rev4_CorruptHpth.ncd	FabA032C279Rev4_Do...	excel8r2	root	1-4	
FabA032C279Rev4_Npth.ncd	FabA032C279Rev4_Npt...	excel8r2	non-plated	1-4	
FabA032C279Rev4_Pst.ncd	FabA032C279Rev4_Pst...	excel8r2	plated	1-4	
FabA032C279Rev4_PPC356.pdf	FabA032C279Rev4_PC...	ipc356	related	bottom	
FabA032C279Rev4_Outline.gbr	FabA032C279Rev4_Out...	ger274r	outline	none	
FabA032C279Rev4_Outline.gbr	FabA032C279Rev4_Out...	ger274r	cut_outline	none	
A0329W02229Rev4.pdf		external	document		
A0329C9843Rev4_TTM.pdf		external	document		
FabA032C279Rev4_Unknown_Shortc_Openst.txt		text	document		
matrix		matrix	document		

Technology

PCB Finish

Material Type

FR4 TG 135
FR4 TG 135 Improved
FR4 TG 150
FR4 TG 175
FR4 TG 175 + Carbonfibre

Netko 9000 Series
Netko 4000-13 (teflon)
Netko 7000-2H1 (polyamide)
Rogers RO-4003 (teflon)
Rogers 6002 (teflon)
Rogers 6000 (teflon)
Rogers R-Flex 3000 (LPC)
Taconic (teflon)
Atron (polyamide)
Bergquist TCP 1000 (aluminium)

S	Significant Characteristic	4	4
P	Process Characteristic	2	1
PTC	Pass Through Characteristic	-	-

References

ZF Worksheet No	Description
IPC 17-02	Regulation of Special Characteristics (SC) Within the ZF Group
ZFN 1127	Technical Specification for Printed Circuit Boards
ZFN 9003	Control of Prohibited and Regulated Substances

Technical Requirements

Item No	Applicability	Requirement	Special Characteristic
1	Applicable	PCB application: Automotive Sealing	
2	Applicable	General PCB requirements shall be according to ZFN 1127	
3	Applicable	PCB shall be fabricated according to IPC 4101 Class 2	
4	Applicable	Characteristics shall be according to ZFN 1127 sections 6.1.1, 6.3.2, 7.1, 7.2, 7.3	
5	Applicable	Electrical testing shall be according to ZFN 1127 section 6.2	
6	Applicable	PCBs or panel shall be marked with a clearly visible electrical test pass mark	
7	Applicable	All materials shall be UL qualified according to ZFN 1127 section 4.14	
8	Applicable	Prohibited substances shall be controlled according to ZFN 9003	
9	Applicable	Laminate material shall be according to IPC-4101/99 12/50 Dwg. MIN	
10	Applicable	Laminate material shall be Strongly Restricted 1	
11	Applicable	Surface finish shall be Immersion Tin according to ZFN 1127 section 6.5	S 1
12	Applicable	Solder mask shall be according to ZFN 1127 section 6.11	
13	Applicable	Solder mask qualification and performance shall be according to ZFN 1127 section 6.11	
14	Applicable	Solder mask material shall be Type PCB-600A/B/0315	
15	Applicable	Solder mask thickness shall be according to ZFN 1127 section 6.11	
16	Applicable	Alkalic solder mask composition shall be according to ZFN 1127 section 6.9	
17	Not Applicable	Silk screen (legend and material data) shall be in one file, version 11/2017	
18	Applicable	Uncoverable hole position markers shall be according to ZFN 1127 section 6.8	P 2
19	Applicable	Copper plating thickness of compliant pin holes identified in the drill table of the PCB drawing shall be according to ZFN 1127 section 6.12	S 2
20	Applicable	Minimum copper plating thickness on all mechanically drilled plated through and barrel plated holes shall be according to ZFN 1127 section 6.12	
21	Not Applicable	Minimum copper plating thickness on all mechanically drilled plated holes shall be 20 µm	
22	Not Applicable	Minimum copper plating thickness on all laser drilled plated holes shall be according to ZFN 1127 section 6.13	
23	Not Applicable	In case of applicable items - Mask remains visible shall be filled with copper	
24	Applicable	Panel outline, correct outline and internal shapes shall be Boardcut	

- Drill Data Review and Edit

- Reviewing correct drill tool attribute assignment:

- End diameter and tool diameter
 - Plated/non-plated recognition
 - Laser drilling recognition
 - Tool function (via, component,...)
 - Hole and slot counts

- Rectifying any inconsistencies in the drill tool attributes and registering the updated information with the Integr8tor Nexus server

data_for_demo.job

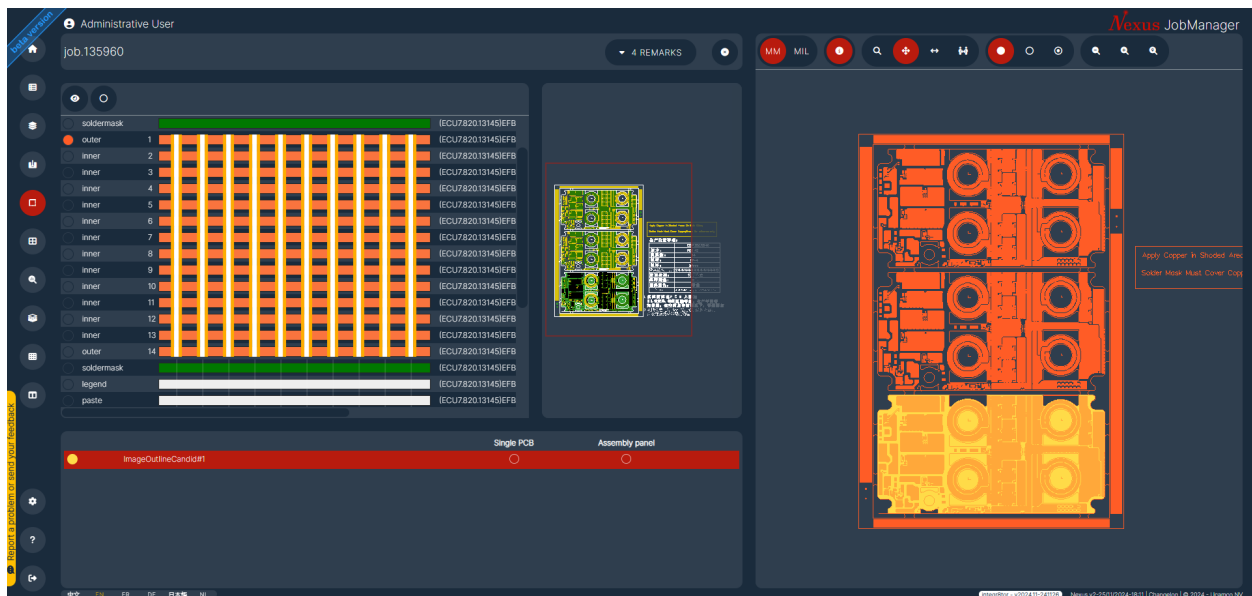
Function

Function	Renamed
solderpaste	top_paste
legend	top_overlay
soldermask	top_solder
signal 1	t1
signal 2	s1
signal 3	s2
power_ground 4	pl1
power_ground 5	pl2
signal 6	s3
signal 7	s4
signal 8	t1
soldermask	bottom_solder
legend	bottom_overlay

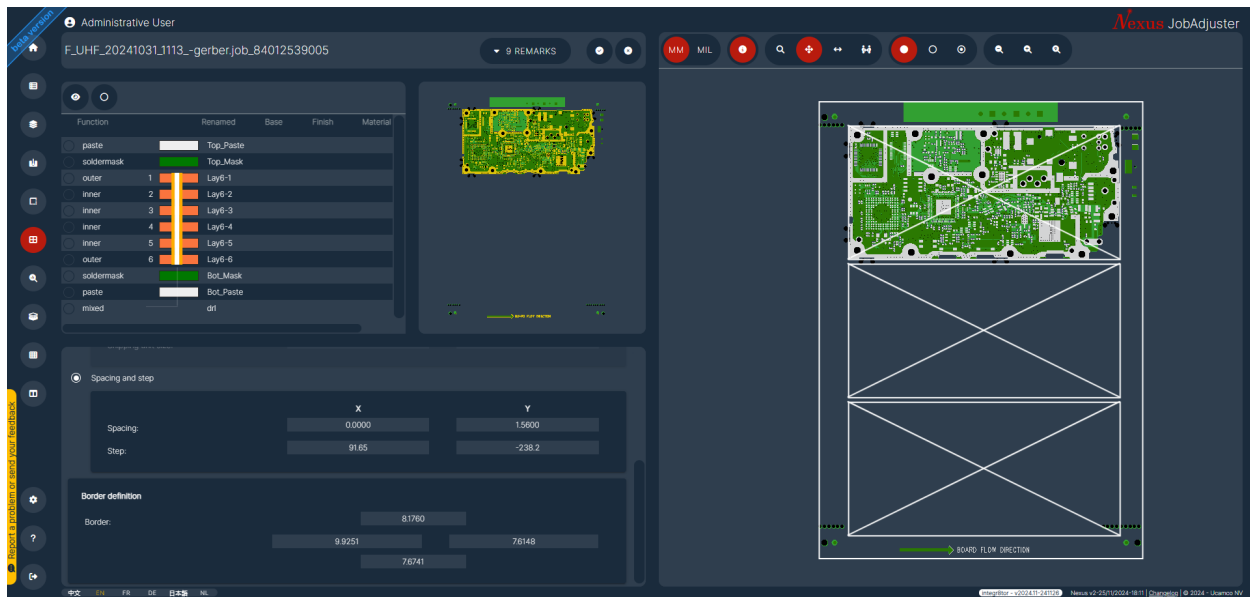
Tolerance: Adjustment:

Layer	Initial	Plating	Nr	End dia	Tool dia
drill_micro_vias_top-mid	drill_micro_vias_top-mid...	plated	0	0.1	0
drill_micro_vias_top-mid3	drill_micro_vias_top-mid...	plated	0	0.1	0
drill_non-plated_bottom	drill_non-plated_bottom...	non-plated	0	0.991	0
drill_non-plated_bottom	drill_non-plated_bottom...	non-plated	2	1.5	0
drill_non-plated_bottom	drill_non-plated_bottom...	non-plated	3	3.2	0
drill_non-plated_bottom	drill_non-plated_bottom...	non-plated	4	3.25	0
drill_non-plated_bottom	drill_non-plated_bottom...	non-plated	1	1.27	0
drill_plated_bottom-top	drill_plated_bottom-top...	plated	0	0.1	0
drill_plated_bottom-top	drill_plated_bottom-top...	plated	1	0.15	0
drill_plated_bottom-top	drill_plated_bottom-top...	plated	2	0.8	0
drill_plated_bottom-top	drill_plated_bottom-top...	plated	3	0.89	0
drill_plated_bottom-top	drill_plated_bottom-top...	plated	5	1.02	0

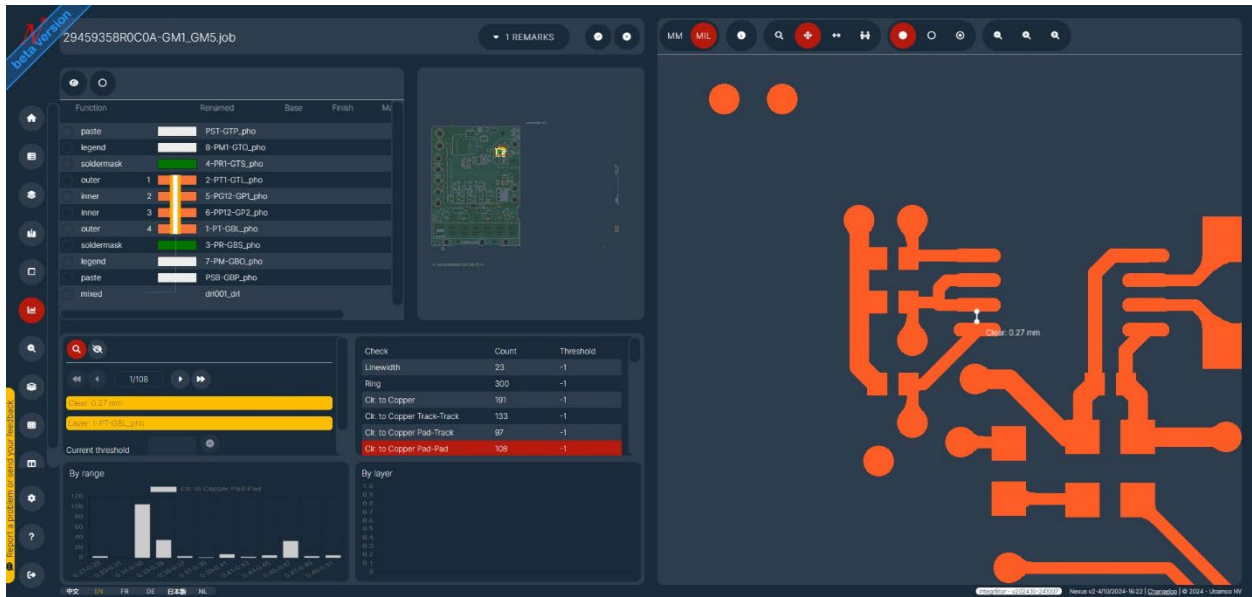
- Outline Review and Edit
 - Reviewing one or several potential outline candidates the system has discovered while searching for the best possible PCB or assembly panel outline
 - **Picking such an outline candidate and promoting it to become the job or assembly panel outline**
 - **Manually selecting chains of vectors from other layers and declare them as the job or assembly panel outline (under development)**



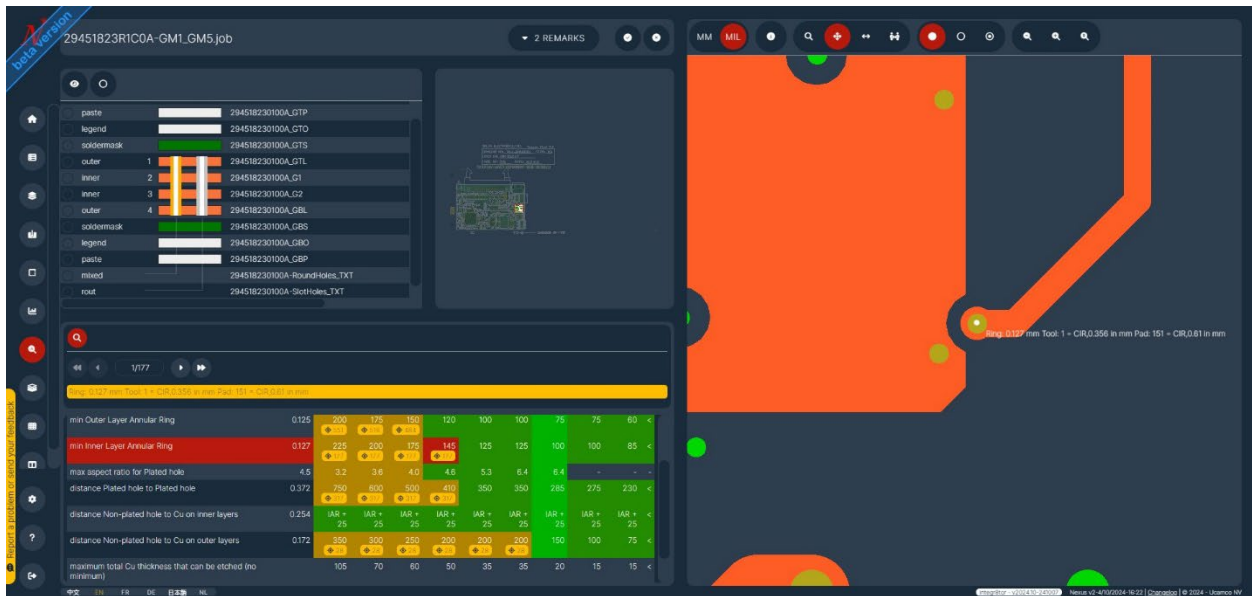
- Sizes Review and Edit
 - Reviewing an assembly panel layout, either manually defined or resulting from the assembly panel auto-recognition server module (licensed – under development)
 - Checking the correctness of the number of repeats in X and Y, the step values, and the size and location of the rail areas
 - Validating the correct single image definition in case of incoming flat panel data
 - **Overwriting any of these settings and saving them to the Integr8tor Nexus server**
 - **Triggering a partial reprocessing of the job and update of QED documentation**



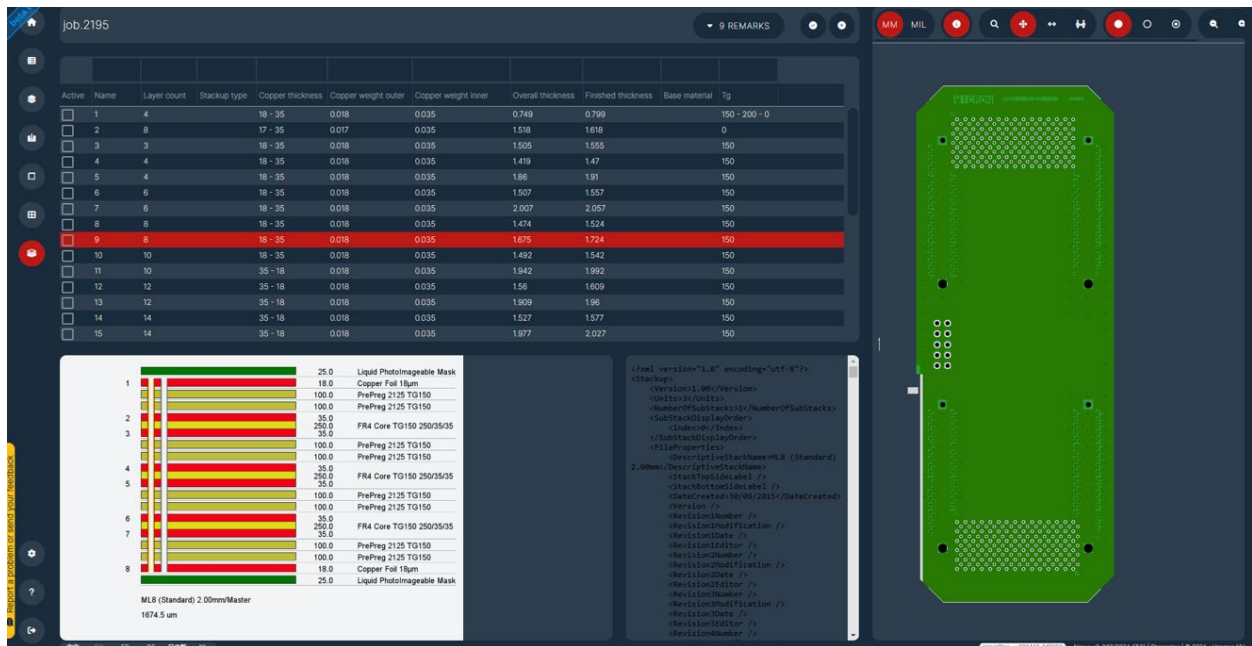
- Hotspot Review and Thresholding (Integr8tor Nexus hotspot server license required for generating analysis data)
 - Reviewing smallest values from the analysis results and locating them on the board
 - Supporting fully dynamic, high resolution PCB graphics with on-screen feedback
 - Including feature measurements and query functionality to validate analysis results
 - Filtering on hotspot type, PCB layer or value range
 - **Blanking out non-relevant analysis values and replacing them with a higher, more pertinent value and location in QED PDF (under development)**



- Capabilities Review (Integr8tor Nexus DFM Classes server license required for generating analysis data)
 - Consulting the results of a board's manufacturability analysis
 - Locating the features on the board responsible for pushing it into a higher manufacturability class
 - Supporting fully dynamic high-resolution PCB graphics with on-screen manufacturing class violation feedback
 - Including feature measurement and query capabilities

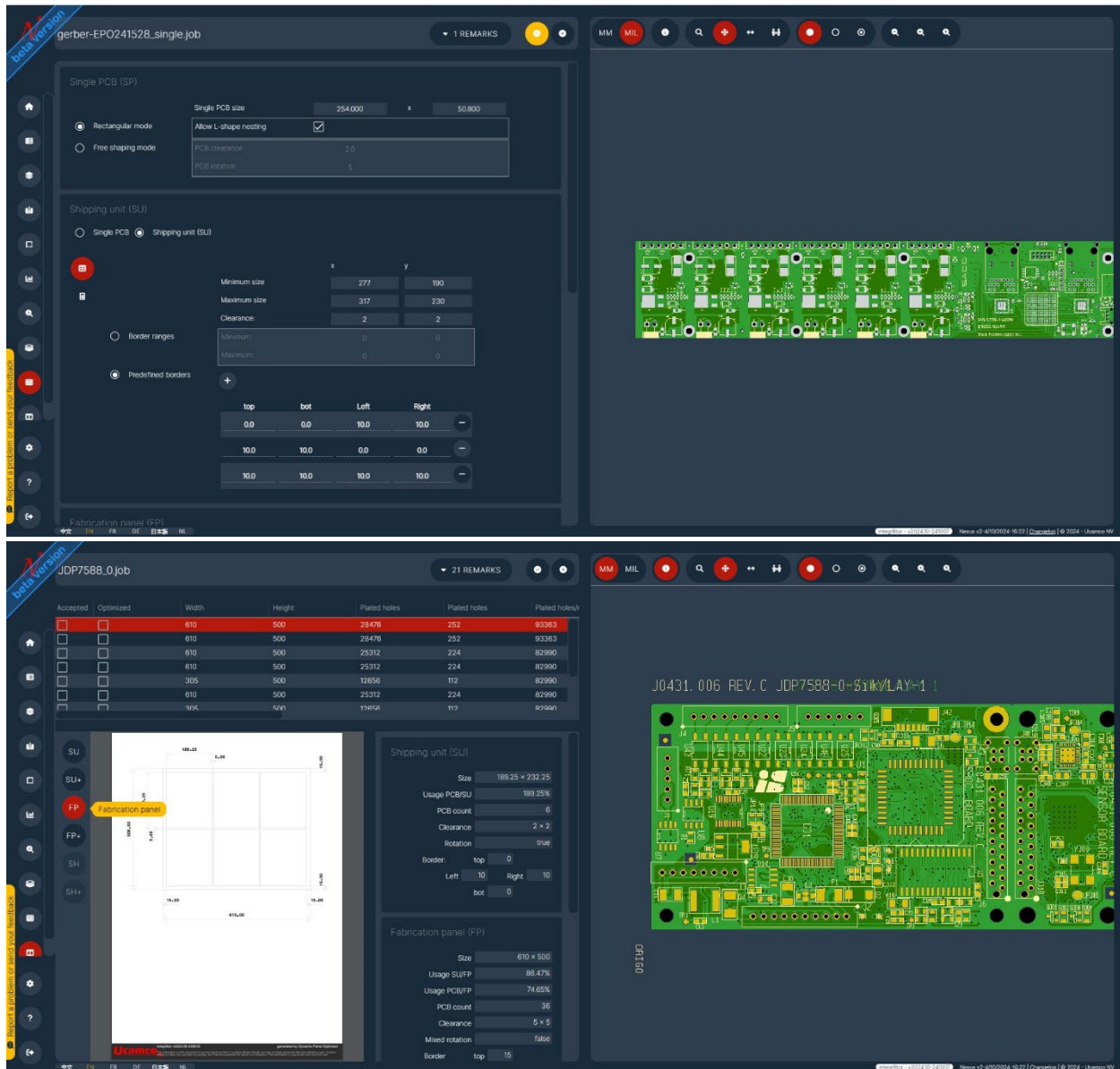


- Stackup assignment from library (Integr8tor Nexus Stackup Template Library server license required)
 - Checking for existing stackups in the library on the server to match customer requirements in terms of
 - Finished stackup thickness
 - Layer count
 - Base material requirements
 - PCB type
 - Copper weights
 - Tg value
 - Identifying “closest fit” if no 100% match turns out available
 - **Selecting a stackup from the templates library and attaching it to the job**



- Panel Setup and Layouts Review (Integr8tor Nexus Dynamic Panel Optimizer server license required for generating analysis data)
 - Reviewing different assembly and fabrication panel layout solutions, optionally with the corresponding material sheet cutting proposals
 - Identifying the solutions that provide best yield or material utilization
 - Dimensioned drawings and real product PDF panel images with textual feedback

- Modifying DPO's default nesting mode, rotation/spacing constraints, panel, border and rail size settings, and triggering a new run of the Panel Optimizer
- Selecting the panel solution to be included on QED PDF



- Software setup and configuration
 - Dedicated environment for system setup and Nexus client user interface configuration

Server

Units

Unit
mm

Decimals
3

Paths

Work folder
C:\Users\lucam\data\8_output\%customer%\%pid%

Utrack exchange folder
C:\tmp\8polar\%pid%

OpenOffice folder
C:\Program Files\LibreOffice

Session server

Min port range (> 1000)
30000

Max port range (> 1000)
60000

Max idle time (> 0)
3600000

ms